

Space Charge Limited Currents from a Strip Shaped Emitter in a Parallel Plate Geometry

*P. Ramesh, S. Jeyadev and Z. D. Popovic
Xerox Corporation*

Abstract

Microscopic electrical defects in the photoreceptor termed Charge Deficient Spots (CDSs) can give rise to objectionable print defects in xerographic systems such as laser printers. Theoretical calculations of Space Charge Limited Currents (SCLC) between concentric cylindrical and spherical electrodes show that a finite SCLC can be sustained, for a finite applied voltage, even as the radius of the emitting electrode goes to zero. Numerical simulations¹ of the SCLC in a model system consisting of a ground plane with a finite circular emitting region and a parallel collector kept at constant applied potential have shown that the SCLC varies *linearly* with emitter radius for small radii. In this paper we consider the emitting region to be an infinitely long strip of finite width, which may be termed a Charge Deficient Line (CDL). Using the same numerical technique, we address the following questions: i) does the current vanish as the width of the CDL vanishes and ii) what is dependence of the current on the width? Finite element methods were used to calculate the SCLC consistent with Maxwell's equations and the correct boundary conditions. Our results show that, for a fixed applied voltage, the SCLC varies linearly with emitter width for large width and becomes *independent* of width for small width.

Introduction

The phenomenon of Space Charge Limited Current (SCLC) has been of considerable scientific and engineering interest for sometime. SCLC is observed in a wide variety of low conductivity materials: inorganic and organic solids^{2,3}, liquids^{4,5} and gases⁶. SCLC has been employed as a tool for determining the mobility of injected charges^{2,6} and, in the case of solids, a considerable amount of theoretical work has been done to understand the role of traps in the samples used in experimental studies^{2,7} on the steady state and transient SCLC responses. Most of the theoretical work on SCLC has focused on infinite electrodes in the plane and cylindrical geometries. In ideal SCLC the electric field at the emitter is zero, by definition, and this leads to infinite charge densities at that electrode. The steady state and transient responses for the case of non-ideal SCLC, where there is a non-zero field at the emitter has been studied^{8,9} in order to better approximate experimental

conditions. The finite field at the emitter is usually associated with the threshold field for injection at the emitter electrode-sample interface.

While the theoretical work is largely confined to regular, infinite geometries many practical devices (e.g. solid state semiconductor chips and xerographic photoreceptors) which have finite domains and complex electrode geometries can operate in the SCLC regime. Calculations of the steady state (i. e. current-voltage or I-V characteristics) and transient responses of the SCLC in such cases require computational methods. Such methods are essentially simulations of the behaviour of the device that is subjected to some well specified external driving forces such as, for example, a voltage. In a previous paper¹, we carried out such an analysis for the case of a circular, finite sized emitting region and studied the dependence of the SCLC on the radius of the region. It was shown that, for a fixed applied voltage, the SCLC varies *linearly* with emitter radius for small radii and goes over to a quadratic dependence for large radii. A plausible scaling argument was presented to explain the linear dependence for small radii. However, it must be borne in mind that theoretical analysis of SCLC in 2 and 3 dimensional geometries (between, respectively, infinitely long concentric cylinders and between concentric spheres) shows that a finite SCLC can be sustained by finite potentials even as the dimension of the emitting inner electrode is reduced to zero².

In this paper we present the results of a simulation of unipolar charge transport in the SCLC limit in the usual parallel plate geometry but where the emitting region is a strip of finite width, but of infinite length. The fundamental question we address is the dependence of the current on the width of the emitting region, especially for the case of widths small compared to the electrode spacing. From the practical point of view, our work has bearing on two technologies: 1) xerographic photoreceptors and 2) semiconductor chips. If the SCLC can be sustained in the planar geometry by a vanishingly thin linear emitter, then even microscopic linear sources in xerographic photoreceptors can cause defects in the final print¹⁰. In the case of semiconductor chips, the linear interconnects are sources of current into the surrounding insulating medium and so the existence of a SCLC from even infinitely thin connects may provide a limit to how close to each other they can be before crosstalk sets in.

Modern xerographic photoreceptors¹¹ (PRs) are planar, layered devices with a ground plane on one interface. The functional layers consist of a charge generation layer next to the ground plane and a charge transporting layer on top. The total thickness of typical devices is about 25 μm . Typical transport layer materials have mobilities of the order of $10^{-5} \text{ cm}^2/(\text{V}\cdot\text{s})$ are hence readily reach SCLC conditions at applied voltages of a few hundred Volts. In a typical xerographic process^{12,13}, the free surface of the charge transport layer is first uniformly charged and then the device is exposed to light in an image wise fashion¹². Thus, charges are generated in the charge generation layer in an image wise fashion which are then transported to the surface. Upon reaching the surface these charges neutralise the original uniform charge image wise and thus form an electrostatic latent image. This latent image is subsequently developed with toner and the developed toner is then transferred to paper and fused to give the final print. Each step in this process is subject to noise, which results in some loss of quality or the appearance of artifacts in the final print. As most present day printers discharge the areas that have to be developed with toner (Discharged Area Development), any spurious, unintended discharge can give rise to dark areas in what should be plain paper. As the human visual system readily picks up these defects, they have a very adverse effect on print quality. The usual cause of these defects¹⁰ is a localized discharge of the photoreceptor due to sources (often called Charge Deficient Spots - CDSs) either in the charge generation layer or its interface with the ground plane. Due to the increasing output print quality requirements, it is useful to gain an understanding of the nature of these sources. The CDSs observed in practice are better modeled as being circular in shape but linear defects (which we may term Charge Deficient Lines – CDLs) can occur in principle in xerographic printing systems that employ belt PRs. Such belts move over supporting bars and are operated under tension; hence long scratches on the back of the belt are common. Internal stresses caused by such scratches could lead to mechanical failures in the functional layers of the PR that then behave as long thin charge emitting regions. In addition, the belts are wrapped around rollers and the stresses due to the constant flexing could also cause mechanical failures that lead to CDLs. In semiconductor chips the interconnects are sources or sinks of charge and it is vital that the crosstalk between them is minimised. The trend towards miniaturisation has resulted in interconnects getting continually thinner – about 0.18 μm at present. It is interesting to explore how the leakage currents depend on the thickness of the interconnects and see if thinner interconnects do result in less leakage and hence less crosstalk.

Model Description and Calculations

The geometry used for our calculations is shown in Fig. 1. The basic electrode structure corresponds to that of two infinite parallel plates separated by a distance L . A voltage

V is applied across the gap which is taken to be filled with a homogenous material characterized by a dielectric constant ϵ and a mobility μ . Without loss of generality, we may take the negative electrode to correspond to ground and also assume that the emitter lies in this electrode. The actual emitter consists of a strip of halfwidth a , as shown in Fig. 1, and is of infinite length along the y axis which is taken to be perpendicular to the page. Due to translational symmetry along the y axis the problem is two dimensional. In a practical situation, the emitting region would correspond to an ideal Ohmic contact -- a contact that can supply whatever current necessary to have enough space charge in the bulk to drive the electric field just above it to zero. In addition to this, the electrostatic boundary conditions have to be satisfied: the collection electrode must be at a potential V and the ground at zero.

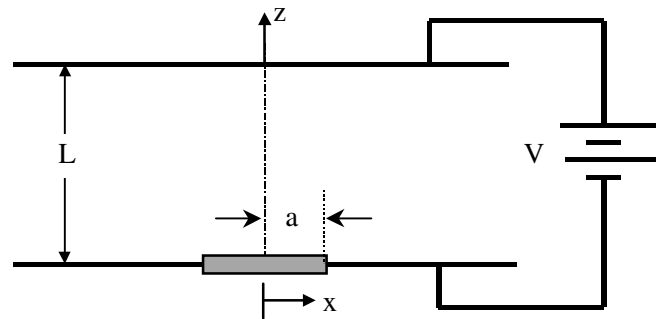


Figure 1. The geometry of the model system. The electrodes are maintained at a potential difference of V . The emitter is a region of halfwidth a in the negative electrode, which is taken to correspond to ground.

The CDL discharge is a space charge limited discharge problem and is governed by the solution to the charge transport equations:

$$\text{div}(\text{grad } \phi) = -\rho/\epsilon \quad (1)$$

$$\delta\rho/\delta t + \text{div } \mathbf{J} = 0 \quad (2)$$

where ϕ is the electrostatic potential, ρ is the space charge density. \mathbf{J} is the current density given by

$$\mathbf{J} = -\rho\mu \text{grad } \phi \quad (3)$$

where μ is the mobility and $\epsilon = K\epsilon_0$ is the permittivity of the media.

The boundary conditions are: at $z = 0$, $\phi = 0$, and at $z = L$, $\phi = V$. Additionally, on the emitter surface: $z = 0$, $|x| \leq a$, $E_z = E_{\text{th}}$ where E_{th} is the threshold field (≈ 0) at which the space charge limited discharge takes place. The initial condition is that at $t = 0$, $\rho = 0$ everywhere in the domain. The simulation of the time evolution of the system is performed until steady state is reached in a manner identical to the case of the CDS calculation¹. The details of the algorithm are available in Refs. 14 and 15.

The actual numerical calculations are carried out employing scaled, dimensionless variables. The scaling used here preserves the fundamental scaling law for all SCLC:

$$I \propto V^2 \quad (4)$$

Because the simulation builds in this scaling into the calculation, we do not explicitly show the I - V characteristics though calculations were carried out to verify the integrity of the model. Also, the same scaling laws show that the total current per unit length is of the form

$$I = \epsilon\mu V^2/L^2 \cdot 2f(a/L) \quad (5)$$

where the function $f()$ depends only on the ratio a/L . However, for the sake of clarity, we will discuss only the total current per unit length I for the case of $L = 25\mu\text{m}$ in what follows. Using the scaling laws our results can be easily used to calculate currents expected for any photoreceptor thickness, emitter width, and applied voltage. We would like to emphasise that the scaling law given in (5) is different in form from that obtained for the axisymmetric case¹ (i.e. the CDS). This is the origin of the difference in the dependence of the SCLC on the CDS radius for small radii and small widths in the case of the CDL.

Results and Discussion

The simulations were run for a PR thickness of $25\mu\text{m}$ and a dielectric constant of 3. Figure 2 shows the dependence of the total current as a function of the CDL full width, $2a$. The dashed lines represent the current per unit length that would have been emitted from a strip of the width as the CDL in the one dimensional parallel plane case, i.e.

$$I_{1d} = 2aJ_{1d} \quad (6)$$

where $J_{1d} = (9/8)\epsilon\mu V^2/L^3$ is the one dimensional SCLC². Thus in the limit $a \gg L$, $f(a/L)$ is equal to $(9/8)a/L$ and the current per unit length depends *linearly* on the width of the CDL. As the width of the CDL shrinks to the order of the PR thickness, the calculated current/length shows an increasing *sublinear* dependence on the width of the strip. For CDL widths much smaller than the PR thickness, the current becomes more and more independent of the width and approaches a finite value as the width of the CDL goes to zero.

Scaling Arguments for Small Width Behaviour

We will now present a general scaling argument which shows that a sublinear relationship is expected between strip width and total current per unit length for small widths and that a finite current persists even as the width of the CDL goes to zero. Let us assume that a flat plate capacitor of thickness L has a localized injection strip of width $2a$, which is much smaller than L (Fig. 1). Under the voltage V applied to the capacitor plates, current per unit length I will flow through the circuit. We can therefore describe the system with four characteristic parameters:

$$L, a, V, I \quad (7)$$

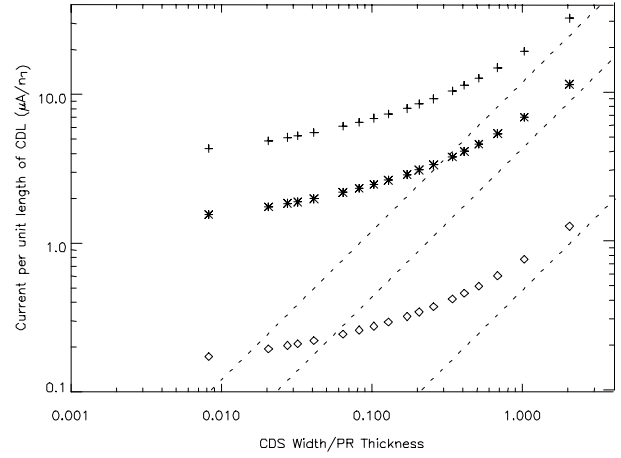


Figure 2. The current per unit length of a CDL as a function of its full width for three fixed applied voltages. Simulation results are denoted by the diamonds (100 V), asterisks (300 V) and pluses (500 V). The dashed lines correspond to the current per unit length in the planar case from a strip of the same width as the CDL, i.e. $2aJ_{1d}$ for the above voltages. The PR thickness is $25\mu\text{m}$ and its dielectric constant is 3. The planar current density is given by $J_{1d} = 4.78 \times 10^{-5} V^2(2a/L) \mu\text{A/m}$.

If we increase linear dimensions of the system by a factor β Equation (5) describing the space charge limited current for a finite width emitter leads to the following scaling of characteristic lengths, voltage and current:

$$\beta L, \beta a, \beta V, I \quad (8)$$

If the charge emitter width, $2a$, is very small (i.e. $a \ll L$), the space charge will be also very small and therefore the electric field inside the capacitor will be approximately constant everywhere, except in the immediate vicinity of the injection spot. Therefore, if we increase the distance between plates by a factor α and the voltage on the capacitor by the same factor, the electric field will remain unchanged and the perturbation on the injection spot, the size of which has remained the same, will be negligible. Consequently the current should also remain the same and the four characteristic parameters of the system for $a \ll L$ will be:

$$\alpha L, a, \alpha V, I \quad (9)$$

If we now scale this last set of system parameters by a factor $1/\alpha$ using Eq.(8) we arrive at:

$$L, a/\alpha, V, I \quad (10)$$

A comparison of (7) and (10) shows that in the limit of small emitter sizes ($a \ll L$) the current remains *unchanged* as the emitter size is decreased by a factor of α with all

other parameters staying the same. In other words, I will be independent of a for $a \ll L$ and $f(a/L)$ will approach a constant value. This is the scaling law obtained by numerical simulation.

Conclusion

In this paper we have carried out a numerical simulation of the SCLC from a strip of finite width and infinite length in the parallel plate geometry. The behavior of the SCLC when the width is large compared to the plate separation approaches that of the planar one dimensional case, with linear dependence on emitter width. When the width of the emitter becomes much smaller than the plate separation, the SCLC shows a *sublinear* dependence on the width and becomes independent of the width of the CDL as the width approaches zero. This behavior can be explained by a scaling argument. The scaling argument implies that the SCLC should become independent of width as the emitter width approaches zero and approach a constant value. These facts are borne out by the numerical simulations. Charge injecting defects in PRs are typically spots (CDSs) and they cause readily detectable print quality defects. CDLs are not seen in practice, but if they were to occur, they would have far more catastrophic impact on print quality as the long, linear streaks on the print would be far more easily perceived. The fact that the SCLC is finite for vanishingly thin emitters has consequences in the semiconductor chip industry. As the trend towards miniaturisation continues and the density of transistors and interconnects increases, there has been an effort to find material packages that reduce the crosstalk between the interconnects. Our results show that because the sublinear SCLC becomes independent of width for very thin emitters the problem is more serious than simple density arguments may indicate.

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Biography

Palghat Ramesh received his Ph.D in Mechanical Engineering from Cornell University in 1988. Since 1990 he has worked at the Wilson Center for Research and Technology at Xerox Corporation in Webster NY. His work is focused on modeling and simulation of xerographic processes.